

SMD POWER COIL-JRPI 0612M SERIES

Reliability and Test Condition

Item	Performance	Test Condition				
Electrical Performance	Test					
Inductance	Refer to standard electrical characteristics	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter				
DCR	list	CH16502,Agilent33420A Micro-Ohm Meter				
Saturation Current (Isat)	Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop △L(%)				
Heat Rated Current (Irms)	Approximately $ riangle$ T40 $^{\circ}$ C	Heat Rated Current (Irms) will cause the coil temperature rise △T(°C) without core loss. 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer				
Operating Temperature	-40°C~+125°C (Including self - temperature	rise)				
Storage Temperature	110~+40℃,50~60% RH (Product without ta 240~+125℃ (on board)	aping)				
Reliability Test						
Life Test		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 125±2°C (Inductor) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs				
Load Humidity	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2% R.H Temperature: 85°C±2°C Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs				
Moisture Resistance		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs, keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.				
Thermal shock		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: $-40\pm2^{\circ}$ C 30 ± 5 min Step2: $25\pm2^{\circ}$ C ≤ 0.5 min Step3: $125\pm2^{\circ}$ C 30 ± 5 min Number of cycles: 500 Measured at room temperature after placing for 24 ± 2 hrs				
Vibration		Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations)				

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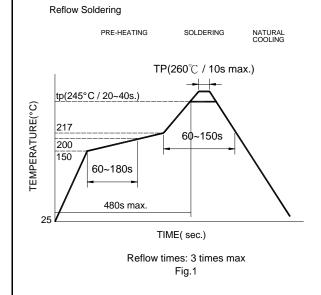
Reliability and Test Condition

Item	Performance	Test Condition							
Reliability Test									
			_	Peak value	Normal	Wave	Velocity		
			Type	(g's)	duration (D)	form	change		
Shock	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value RDC: within ±15% of initial value and	1	SMD	50	(ms)	Half-sine	(Vi)ft/sec		
		╽┝	Lead	50	11	Half-sine	11.3		
			Load	30		Tidii Siric	11.0		
		C	Chall he may inted an a FD4 as it starts of the						
	shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805							
			inch(2012mm):40x100x1.2mm						
Bending			<0805 inch(2012mm):40x100x0.8mm						
			Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm						
		duration of 10 sec.							
	More than 95% of the terminal electrode should be covered with solder	Pi	Preheat: 150℃,60sec						
			Solder: Sn96.5% Ag3% Cu0.5%						
Soderability			Temperature: 245±5°C						
			Flux for lead free: Rosin. 9.5% Dip time: 4±1sec						
		Depth: completely cover the termination							
		D	Depth: completely cover the termination						
		\mathbf{l}			Temp	erature			
Resistance to			Temperatur (°C)	re Time (s)		nmersion	Number of heat cycles		
Soldering Heat			(0)	(0)	and eme	rsion rate	Tiout by blob		
			260 ±5	1 10 +1	25mm/s ±	n/s ±6 mm/s	1		
			(solder tem						
	-	P	recondition	ing:Run thr	ough IR refl	ow for 2 tin	200		
			Preconditioning:Run through IR reflow for 2 times (IPC/JEDEC J-STD-020D Classification Reflow Profiles)						
	Appearance: No damage. Impedance: within±15% of initial value		With the component mounted on a PCB with the device to be tested, apply a force (>0805:1kg, <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually						
	Inductance: within±10% of initial value								
	Q : Shall not exceed the specification value								
	RDC: within ±15% of initial value and				to the comp				
Terminal Strength	shall not exceed the specification value								
			_	UT .	1				
			wide						
			substrate press tool						
						she	ear force		
Nota : When there are	questions concerning measurement result mea	2011	romont ob	all ha mada	ofter 40 L	houro			

Note : When there are questions concerning measurement result measurement shall be made after 48 ± 2 hours Of recovery under the standard condition.

Reliability and Test Condition

Item	Performance	Test Condition				
Soldering and Mounting						
Soldering	Mildly activated rosin fluxes are preferred. JANTEK terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.					
Lead Free Solder re-flow:	Recommended temperature profiles for re-flow soldering in Figure 1.					
Soldering Iron:	Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2. Note: • Preheat circuit and products to 150°C • Never contact the ceramic with the iron tip • Use a 20 watt soldering iron with tip diameter of 1.0mm • 355°C tip temperature (max) • 1.0mm tip diameter (max) • Limit soldering time to 4~5 sec					



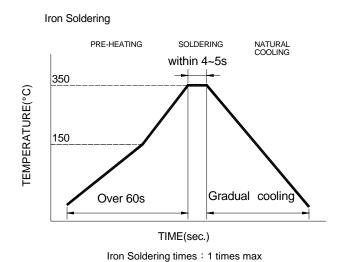


Fig.2